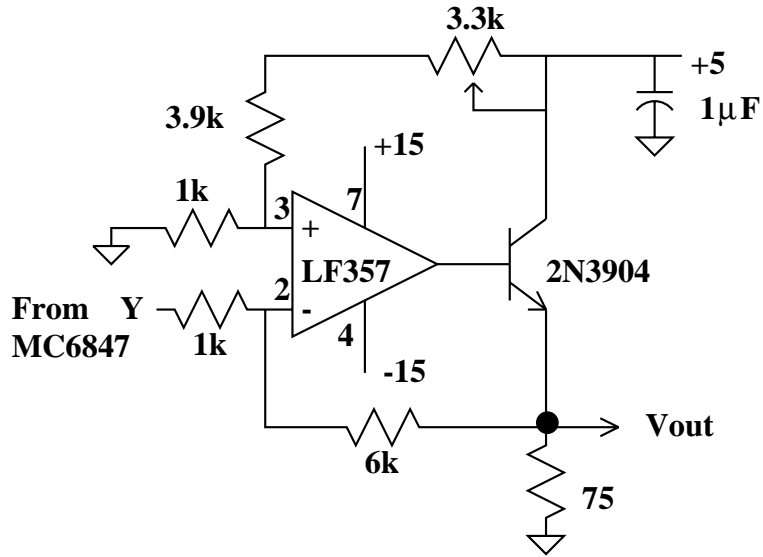


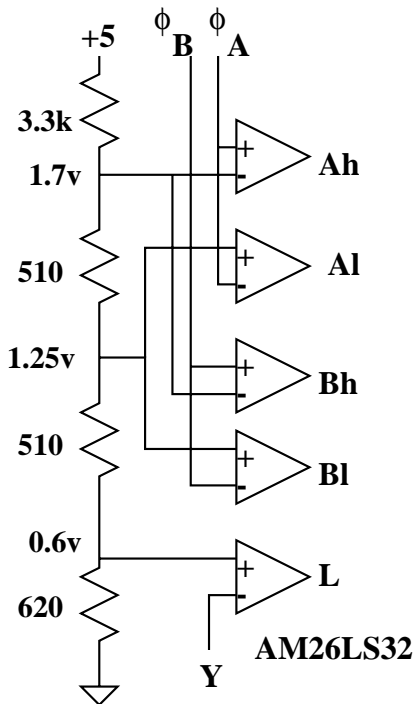
For Black and White Output

Adjust 3.3k pot so bottom of sync pulse has $V=0$

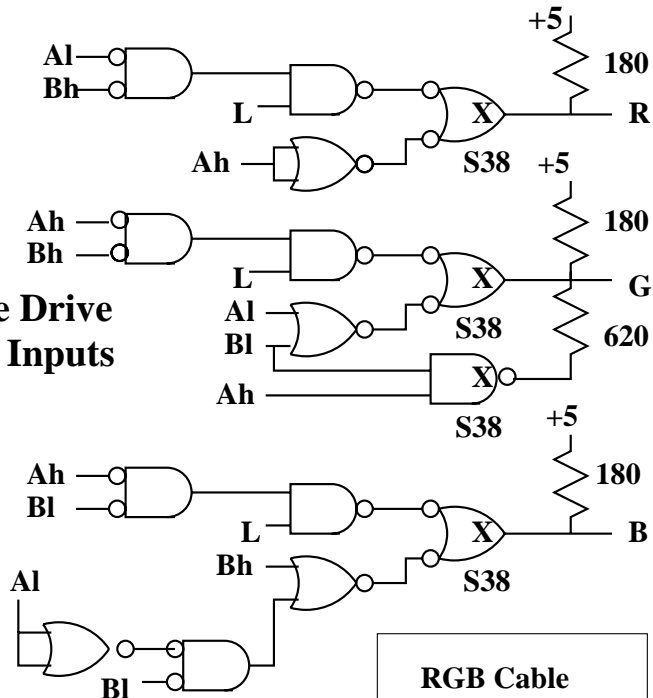


For Color: Decode Chroma

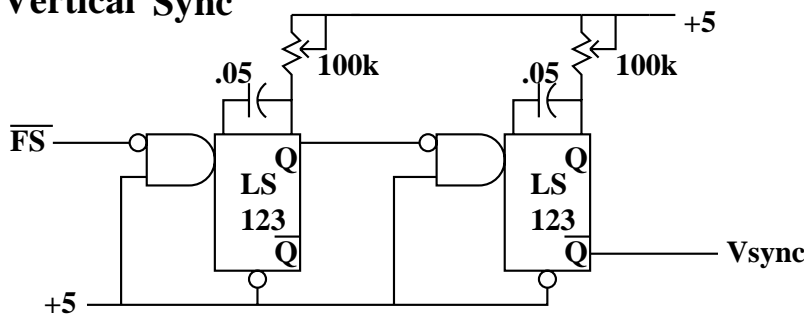
Circuit for a particular mode. Check your mode! Put logic in a PAL.



These Drive RGB Inputs



And provide Vertical Sync

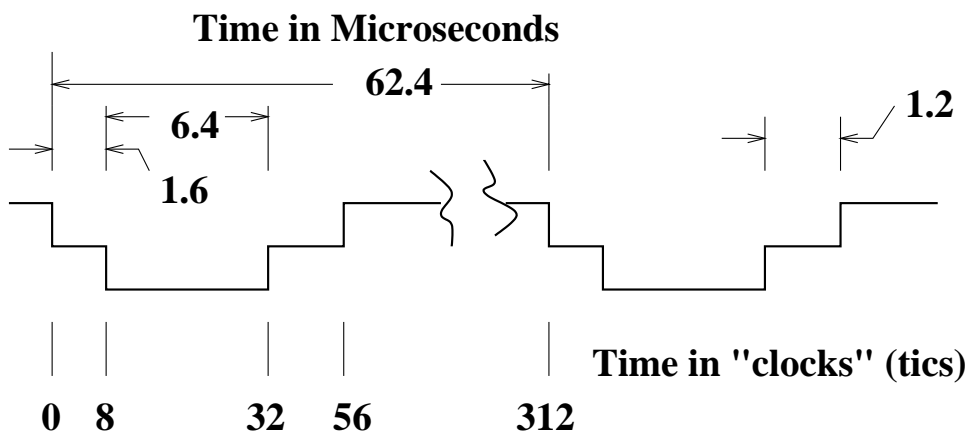


RGB Cable	
Pin 1	Intensity
2	Red
3	Green
4	Blue
5	Gnd
6	Gnd
7	Hsync
8	Vsync

LM1882 Sync Generator (data sheet no. 95)

This is a programmable part and is quite flexible.

Example: 256 bits wide, 256 lines, using a 5 MHz Clock



Register Contents:

Horizontal (Line) Control

R1	9	Horizontal Front Porch	} Time in "clocks"
R2	33	Horizontal Sync Pulse End	
R3	57	Horizontal Blanking	
R4	312	Line Width - must be even	

Vertical (Frame) Control

R5	4	Vertical Front Porch	} Lines
R6	7	Vertical Sync Pulse End	
R7	21	Vertical Blanking	
R8	276	Frame: 256 lines + 20 lines blanking	

Register 0: Contents 011000011000

Bit 10: Enable System Clock

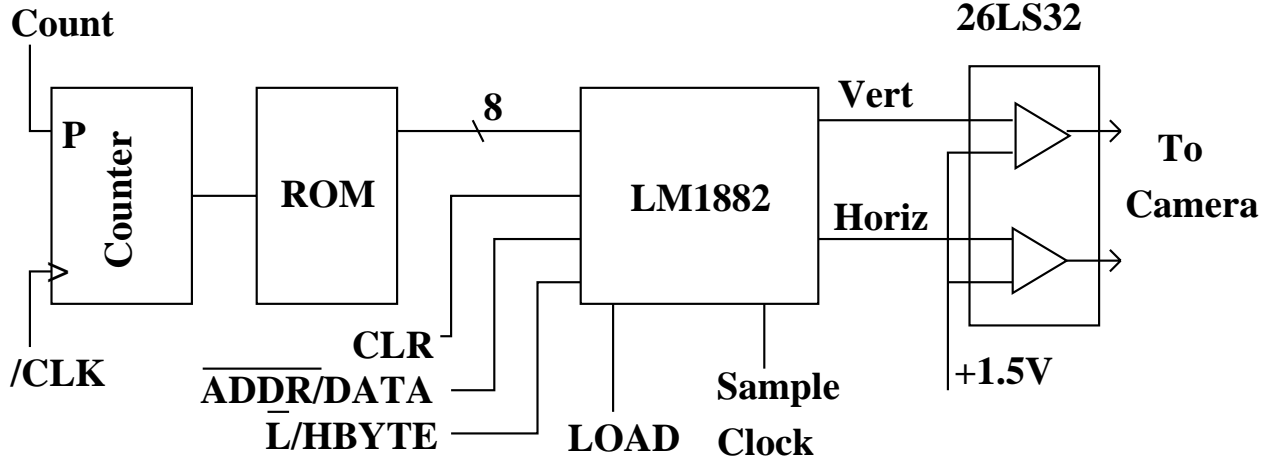
Bit 9: Disable Equalization

Bits 8:5 Sync Pulses Active Low

Bits 4:3 Non-Interlaced

Bits 2:0 Default Output Config:

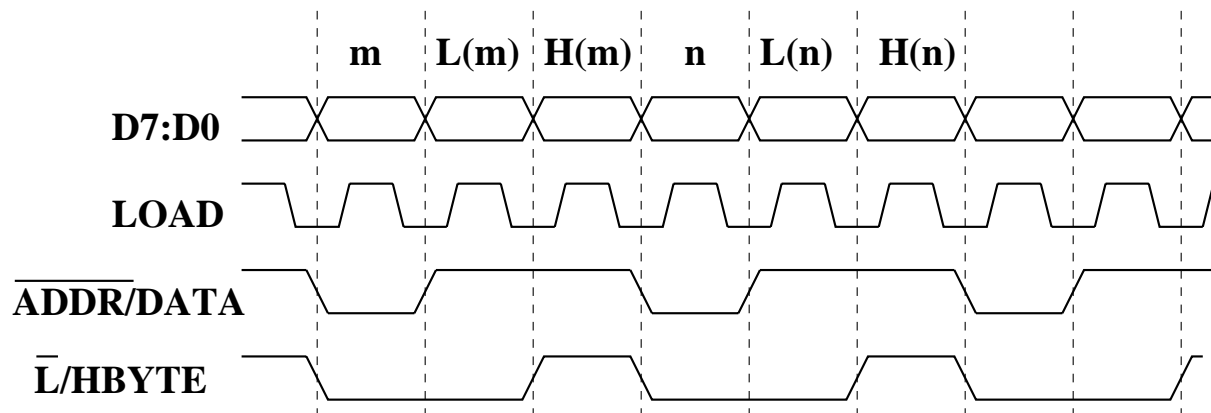
Pin 12 CBLANK, Pin 13 HGATE, Pin14 CSYNC, Pin 15 VGATE



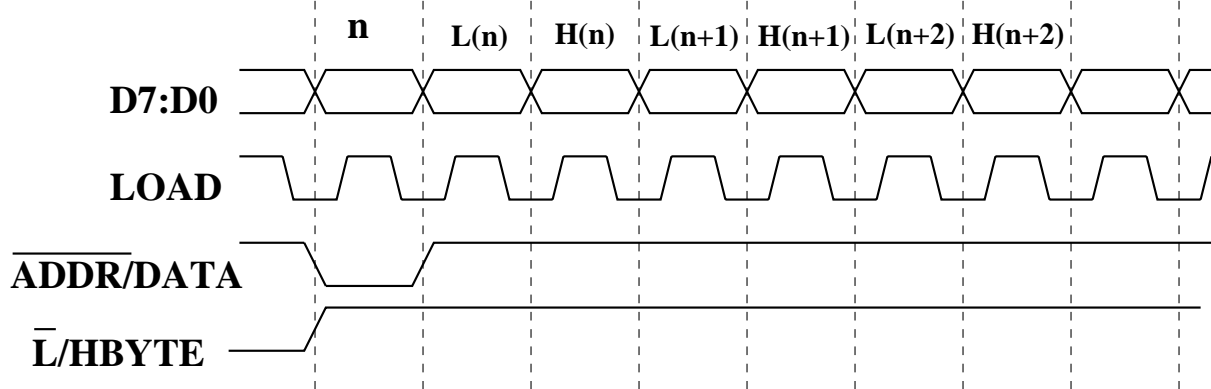
LM1882 Must be loaded on power-up: ROM holds configuration

CLR - Loads RS170 defaults and turns off Clock.

"Manual" Addressing Mode:



"Automatic Addressing Mode"



Sync Separator:

