

# 6.111 Due Dates Spring 2000

Monday	Tuesday	Wednesday	Thursday	Friday
Jan. 31 Reg Day	Feb. 1 Logic Analyzer Demo	2 PS 1 out Lab 1 out	L1	4 L2
7 L3		9 PS 1 due PS 2 out	L4	11 L5
14 Lab 2 out Lab 1 due		16 PS 3 out	L7 PS 2 due	18 L8
21 <i>HOLIDAY</i>	22 Monday Schedule Lab 2 design due	L9 23 PS 3 due	L10	25 QUIZ 1 34-101 (Lecture Hall)
28 PS 4 out Lab 2 Checkoff (option)		Mar 1 Lab 4 out (option) Lab2 checkoff (option) Lab 2 Report (option)	L12	3 <i>(Add Date)</i>
6 Lab 3 out (option) Lab 2 report (option)		8 PS 5 out <i>PS 4 due</i>	L14	10 Lab 4 checkoff (option)
13 Lab 3 MCU checkoff (option) Lab 4 report (option) Lab 5 out (option)		15 Lab 3 design due (option)	L16	17 Lab 5 Design (option) Prob. Set 5 due
20	<b><i>SPRING BREAK</i></b>			
27 Formation of Project Teams		29 Proj. Abstracts due	L18	31 Lab 5 checkoff (option) Lab 3 Checkoff (option)
April 3 Lab 3 Report due (option) Lab 5 report (option) Proposal Conference		5 Proposal Conference	L20	7 QUIZ 2 50 – 340
10 Block Diagram Conference		12 Block Diagram Conference		14 Block Diagram Conference
17 <i>HOLIDAY</i>	<i>HOLIDAY</i>	19 Project Presentations in 34-101	20 <i>DROP DATE Last Day to add 6.905</i>	21 Project Presentations in 34-101
24 Project Presentations in 34-101		26 Project Presentations in 34-101		28 DEBUG
May 1 DEBUG		3 DEBUG		5 DEBUG
8 Project Demonstrations DEBUG	Project Demonstrations	10 Project Demonstrations Video Tape	11 Reports due by 5:00 P.M.	