

6.111 Due Dates

Spring 2002

Monday	Tuesday	Wednesday	Thursday	Friday
Feb. 4 Registration Day	Feb. 5 Logic Analyzer Demo	6 L1 PS 1 out Lab 1 out		8 L2
11 L3		13 L4 PS 1 due PS 2 out		15 L5
18 <i>HOLIDAY</i>	19 L6 Monday Schedule	20 L7 Lab 1 due PS3 out PS 2 due Lab 2 out		22 L8
25 L9	26 Lab 2 design due	27 L10 PS 3 due		March 1 Quiz 1 (50-340)
March 4 L11 PS 4 out		6 L12	7	8 L13 Lab 2 checkoff <i>ADD DATE</i>
11 L14 Lab 2 report due Lab 3 out		13 L15 PS 5 out PS 4 due	14 Revised Lab 2 report due for Phase II	15 L16
18 L17 Lab 3 MCU checkoff		20 L18 Lab 3 design due		22 L19 PS 5 due
25	<i>SPRING BREAK</i>			29
April 1 L20 Formation of Project Teams		3 L21 Project Abstracts due		5 L22 Lab 3 checkoff
8 L23 Lab 3 report due Proposal Conference	Proposal Conference	10 L24 Proposal Conference	<i>No More Lectures</i>	12 Quiz 2 (50-340)
15 <i>HOLIDAY</i>	16 <i>HOLIDAY</i>	17 Proposal Conference	18 Block Diagram Conference	19 Block Diagram Conference
22 Block Diagram Conference	23 Block Diagram Conference	24 Block Diagram Conf. <i>Last Day 6.905 Forms Signed</i>	25 <i>DROP DATE</i> Last Day to Add 6.905	26 Project Design Presentations in 34-101
29 Project Design Presentations in 34-101		May 1 Project Design Presentations in 34-101		2 Project Design Presentations in 34-101
6 WIRE / DEBUG		8 WIRE / DEBUG		10 WIRE / DEBUG
13 DEBUG	Project Demonstrations	15 Project Demonstrations Video Taping	16 Final Project Reports due by 5:00 P.M.	